Docket No.: 42P16840

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

CHRISTOPHER L. RUMER, ET AL.

Art Group:

Application No.:

Examiner:

Filed:

For: METHOD AND APPARATUS FOR A DUAL SUBSTRATE PACKAGE

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: September 30, 2003

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Carla Vignola

09-30-03

Date

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				C	Complete if Known		
				Application Number			
				Filing Date			
				First Named Inventor	Christopher L. Rumer		
				Art Unit			
				Examiner Name			
Sheet	1	0	f 1	Attorney Docket Number	42P16840		

NON PATENT LITERATURE DOCUMENTS							
Examiner Initials*	Cite No.'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		RECHE, J., "Wafer Thinning and Thru-Silicon Vias", The Path to Wafer Level Packaging, 41 pages, Tru-Si Technologies, www.trusi.com, IEEE/CPMT Meeting, Santa Clara (CA)					
		05/10/2000.					
Eversines							

Examiner	 Date	
Signature	Considered	

<sup>\*</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>&#</sup>x27;Applicant's unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.